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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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AUG 04 2004

Applicant: Jacson Liu Examiner: Guerrero, Maria F
5 Filing Date: 11/03/1998 Art Unit: 2822
App. No.: 09/187,197 Docket No.: MVIP0005USA

Title: Shallow Trench Isolation Method for a Semiconductor Wafer

OFFICIAL

10 To: Commissioner for Patents
P.O. BOX 1450
Alexandria, VA 22313-1450

15 Subject: Response to the Office Action dated 09/22/2003

Dear Sir/Madam:

INTRODUCTORY COMMENTS

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In response to the Office Action identified above, the above-identified application is to be amended as indicated in the following sections. Consideration of all amendments is politely requested.